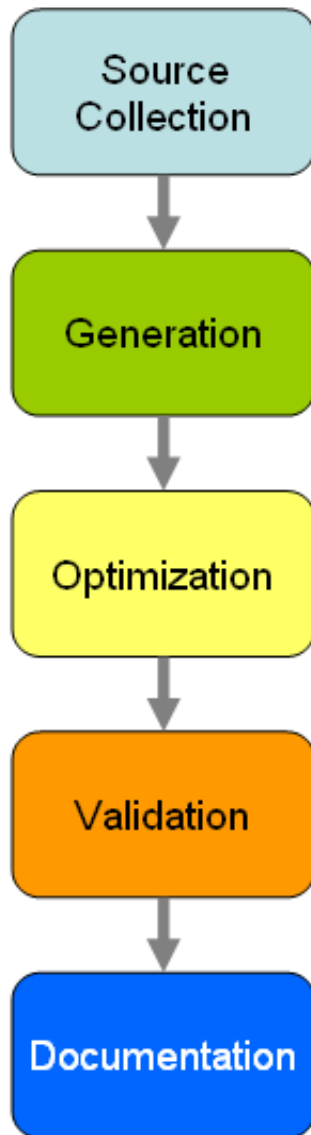


SI (Signal Integrity) Modeling Service

– The solution for your SI simulation and accuracy issues

When you are ready for SI simulations, SI Modeling is the first issue for you to be solved. It is important not only for simulation setups but also for simulation accuracies since all the simulators will follow “Garbage in, Garbage out” rule for their output results. As a SI modeling expert, we, **IO Methodology Inc.**, provide a series of professional services for model creations and validations. And it covers wide-range of modeling categories and formats from buffer to interconnect, from IBIS to S-parameters, as well as Spice/AMS macromodels and newly approved IBIS AMI models. We are professionals for SI modeling. We understand your needs and concerns. We help you to have your simulation setup correctly and run accurately.



We offer IBIS, Spice and AMS Macromodel, IBIS AMI model and Interconnect model services

IBIS Model Generation, Optimizations and Validation Service

- IBIS model generations from Spice decks or measurement results
- IBIS model optimizations against specification, golden source and multi-simulator environment
- IBIS model validations using “application level” techniques for professional verifications
- IBIS Certification Report, Validation Report, Analysis Report are available for customers.
- IBIS models for single-end, pseudo and true differentials, multi-stages, on-die-terminations (ODT), series switches and ECL using [diff pins], [series switch], [series current], [dynamic clamping], [model selector], [driver schedule], etc.

Macromodel Generation, Optimizations and Validation Service

- Macromodel generations from diagrams, schematics and specifications
- Macromodel optimizations against specification, golden source and multi-simulator environment
- Macromodel validations using “application level” techniques for professional verifications
- Macromodel Certification Report, Validation Report, Analysis Report are available for customers.
- Macromodel formats using Spice (HSpice, Cadence ESpice, etc.), Verilog-A(MS) and VHDL-AMS
- Macromodels for pre-emphasis, de-emphasis, DEF, FFE, CDR, etc.

IBIS AMI (Algorithmic Model) Generation, Optimizations and Validation Service

- IBIS AMI model generations from algorithms, algorithm code and specifications
- IBIS AMI optimizations against specification, golden source, measurement and multi-simulator environment
- IBIS AMI model validations using “application level” techniques for professional verifications
- IBIS AMI Certification Report, Validation Report, Analysis Report are available for customers.
- IBIS AMI for algorithm-based FFE, DFE, CDR and tap optimizations

Interconnect Model Generation, Optimizations and Validation Service

- Interconnect model generations from physical structures and measurement results
- Interconnect model optimizations collocates with measurement and multi-simulator environment for passivity and causality
- Interconnect model validations using “application level” techniques for professional verifications
- Interconnect model Certification Report, Validation Report, Analysis Report are available for customers.
- Interconnect model for via, stub, serpentine pattern, transmission line, package, etc. using Spice RLGC lump elements, S-parameter, IBIS ICM, etc.